

Atty. Docket No. NLMI.P211

PATENT

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of

First Named Inventor: Srinivasan, Varadarajan

Application No.: 10/613,628

Filed: 7/2/2003

For: METHOD AND APPARATUS FOR THROTTLING
SELECTED TRAFFIC FLOWS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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Examiner:

Art Unit: 2661

Confirmation 4351
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Sir:

Enclosed is an Information Disclosure Citation Form 1449/PTO together with a copy of each reference cited therein, excluding U.S. Patents and Published U.S. Patent Applications. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form 1449/PTO be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

X 37 C.F.R. §1.97(b).

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 Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 under 37 C.F.R. §1.17(p) for the submission of this Information Disclosure Statement; or

 A statement pursuant to 37 C.F.R. §1.97(e) is attached hereto.



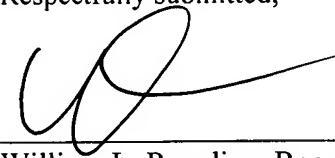
37 C.F.R. §1.97(d). Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 and a statement pursuant to 37 C.F.R. §1.97(e) is enclosed.

Pursuant to 37 C.F.R. 1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fee deficiency in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

Date 12/1/2006



William L. Paradice, Reg. No. 38,990
Tel. 408-236-6646



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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First Inventor	Srinivasan, Varadarajan
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Non Patent Literature Documents

Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Translation
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	Agere Ads Additional PaloadPlus Processor to Product Line, Agere Press Release, November 29, 2000 (11/29/00), 3 pgs.	
	Architecture and Design of Function Specific Wire-Speed Routers for Optical Internetworking, published by Entridia Corp., December 6, 2000 (12/6/00), 60 pgs.	
	ATLAS I: A General-Purpose, Single-Chip ATM Switch with Credit-Based Flow Control, IEEE Hot Interconnects IV Symposium Proceedings, Standford, CA, Pub. August 15-17, 1996, 11 pgs.	
	CSIX-L1:Common Switch Interface Specification-L1, published by CSIX, August 5, 2000(8/5/2000) , 72 pgs.	

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	PMC-Sierra's ATM Chip Set Provides the Traffic Management and Switch Fabric Core for Ericsson's AXD 301 ATM Switch, PMC-Sierra, Publ. December 12, 2000 (12/12/00), 10 Pgs.	
	Scalable Hardware Earliest-Deadline-First Scheduler for ATM Switching Networks, 18th IEEE Real-Time Systems Symposium, Pub. 1997, 9 pgs.	
	Simulation Study of Statistical Delays in an ATM Switch Using EDF Scheduling, Dept. of Computer Science, North Carolina State University, Pub. June 24, 1999 (6/24/99), 25 pgs.	
	Smart Memory Technology in the MAN, Silicon Access Networks, Pub. October 2000, 6 pgs	
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	Start-time Queuing: A Scheduling Algorithm for Integrated Services Packet Switching Networks, Pawan Goyal, Harrick M. Vin and Haichén Cheng, Distributed Multimedia Computing Laboratory, Dept. Computer Sciences, University of Texas at Austin, Pub. 1996, 12	

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	Vitesse Announces Industry's First OC-48c Traffic Management Engine, Vitesse Semiconductor Corp., Sept. 28, 2000 (9/28/00), 2 pgs.	
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	Zettacom: Hurry Up and Wait, www.lightreading.com, Pub. Nov. 29, 2000 (11/29/00), 2 pgs.	

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